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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

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200A-0002

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>AC 83</p> <p>SCANNED</p> <p>Q.A. 65</p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/678318	CONT/PRIOR D	CLASS 257	SUBCLASS 554	ART UNIT. 2815	EXAMINER Brock
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APPLICANTS

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TITLE

Optimized circuit design layout for high performance ball grid array packages

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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